

ABSTRACT

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The present invention concerns a method and a system for generating a global
5 simulation model of an architecture. In particular, the invention concerns a method of
automatic generation, by means of a data processing system (40) associated with a
program called a Configurator for creating a global simulation model of an architecture
comprising models of integrated circuits under development, a method characterized in
that it includes the following steps:

- 10 - the reading of the architecture description file (FDARCH) of the global
model and the storage of information related to all of the possible configurations,
- the instantiation of the components and the storage of the corresponding
information in an instance connection table (TCINST),
- the topological connection of the interface signals,
15 - the automatic generation of HDL-type (MGHDL) and HLL-type (MGHLL)
source files of the global simulation model, corresponding to the configuration specified
by the configuration definition file (FCONF).

Fig. 2A

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